ZXMS6006DT8

60V DUAL N-CHANNEL SELF PROTECTED ENHANCEMENT MODE *INTELLIFET*® MOSFET

Product Summary

- Continuos drain source voltage 60V
- On-state resistance 100mΩ
- Nominal load current (V_{IN} = 5V) 2.8A
- Clamping Energy 210mJ

Description and Applications

The ZXMS6006DT8 is a dual self protected low side MOSFET with logic level input. It integrates over-temperature, over-current, over-voltage (active clamp) and ESD protected logic level functionality. The ZXMS6006DT8 is ideal as a general purpose switch driven from 3.3V or 5V microcontrollers in harsh environments where standard MOSFETs are not rugged enough.

- Lamp Driver
- Motor Driver
- Relay Driver
- Solenoid Driver

Features and Benefits

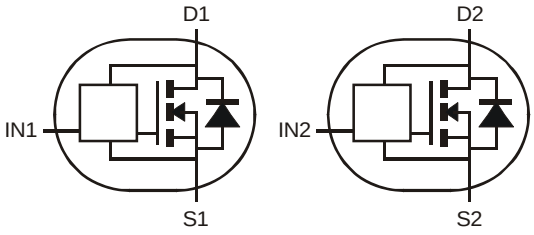
- Compact high power dissipation package
- Low input current
- Logic Level Input (3.3V and 5V)
- Short circuit protection with auto restart
- Over voltage protection (active clamp)
- Thermal shutdown with auto restart
- Over-current protection
- Input Protection (ESD)
- High continuous current rating
- **Green, RoHS Compliant (Note 1)**
- **Halogen and Antimony Free. (Note 2)**
- **Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

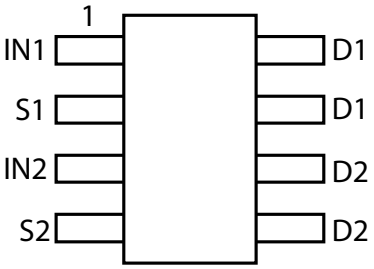
- Case: SM-8
- Case Material: Molded Plastic, “Green” Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish
- Weight: 0.117 grams (approximate)

SM-8

Top View



Device symbol



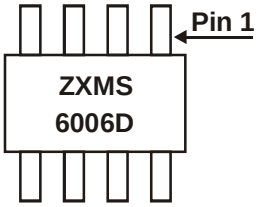
Top view
Pin-Out

Ordering Information (Note 3)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXMS6006DT8TA	ZXMS6006D	7	12	1,000

Notes: 1. Contain <900ppm bromine, chlorine (<1500ppm total) and <1000ppm antimony compounds.
2. Diodes Inc’s “Green” Policy can be found on our website at http://www.diodes.com
3. For packaging details, go to our website at http://www.diodes.com

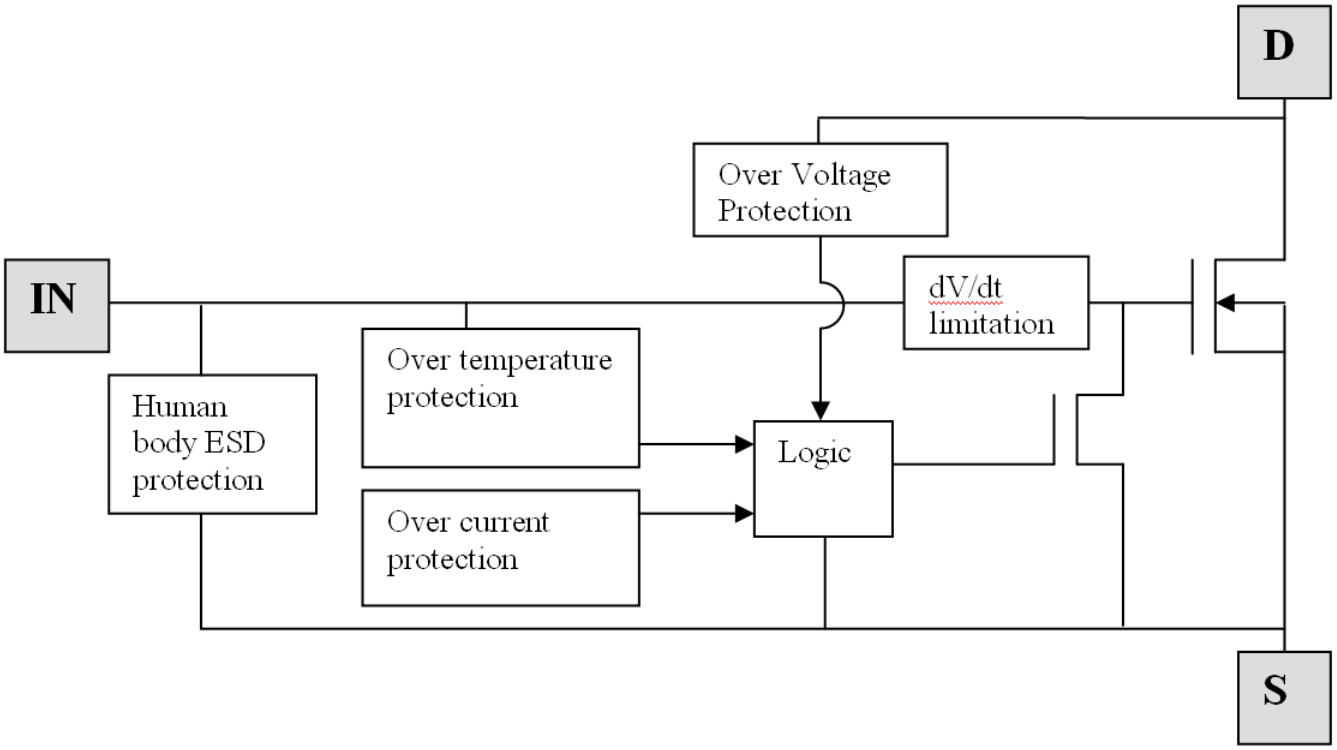
Marking Information



Top View

ZXMS6006D = Product Type Marking Code

Functional Block Diagram



Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units
Continuous Drain-Source Voltage	V_{DS}	60	V
Drain-Source Voltage For Short Circuit Protection	$V_{DS(SC)}$	16	V
Continuous Input Voltage	V_{IN}	-0.5 ... +6	V
Continuous Input Current @-0.2V ≤ V_{IN} ≤ 6V Continuous Input Current @V_{IN} < -0.2V or V_{IN} > 6V	I_{IN}	No limit $\lvert I_{IN} \rvert \leq 2$	mA
Pulsed Drain Current @V_{IN} = 3.3V (Note 6)	I_{DM}	11	A
Pulsed Drain Current @V_{IN} = 5V (Note 6)	I_{DM}	13	A
Continuous Source Current (Body Diode) (Note 4)	I_S	2	A
Pulsed Source Current (Body Diode)	I_{SM}	12	A
Unclamped Single Pulse Inductive Energy, T_J = 25°C, I_D = 0.5A, V_{DD} = 24V	E_{AS}	210	mJ
Electrostatic Discharge (Human Body Model)	V_{ESD}	4000	V
Charged Device Model	V_{CDM}	1000	V

Thermal Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units
Power Dissipation at T_A = 25°C (Notes 4 & 7) Linear Derating Factor	P_D	1.16 9.28	W mW/°C
Power Dissipation at T_A = 25°C (Notes 4 & 8) Linear Derating Factor	P_D	1.67 13.3	W mW/°C
Power Dissipation at T_A = 25°C (Notes 5 & 7) Linear Derating Factor	P_D	2.13 17	W mW/°C
Thermal Resistance, Junction to Ambient (Notes 4 & 7)	$R_{\theta JA}$	108	°C/W
Thermal Resistance, Junction to Ambient (Notes 4 & 8)	$R_{\theta JA}$	75	°C/W
Thermal Resistance, Junction to Case (Notes 5 & 7)	$R_{\theta JC}$	58.7	°C/W
Thermal Resistance, Junction to Case (Note 9)	$R_{\theta JC}$	26.5	°C/W
Operating Temperature Range	T_J	-40 to +150	°C
Storage Temperature Range	T_{STG}	-55 to +150	°C

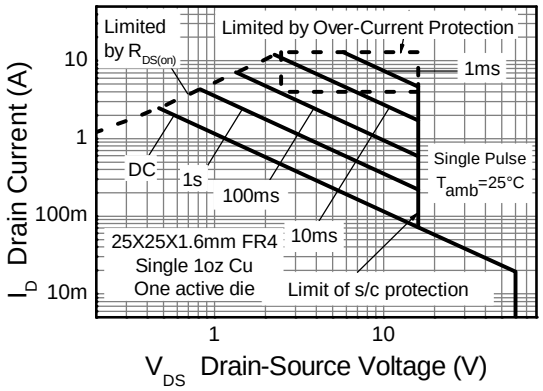
Notes:
4. For a dual device surface mounted on a 25mm x 25mm single sided 1oz weight copper split down the middle on 1.6mm FR4 board, in still air conditions.
5. For a dual device surface mounted on FR4 PCB measured at t≤ 10sec
6. Repetitive rating25mm x 25mm FR4 PCB, D = 0.02, Pulse width = 300µs – pulse width limited by junction temperature. Refer to transient thermal impedance graph.
7. For a dual device with one active die.
8. For a dual device with 2 active die running at equal power.
9. Thermal resistance from junction to the mounting surface of the drain pin.

Recommended Operating Conditions

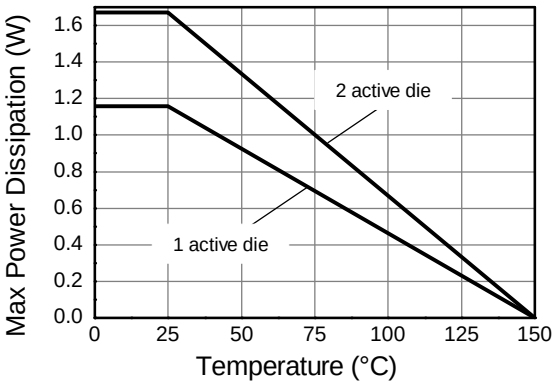
The ZXMS6006DT8 is optimized for use with µC operating from 3.3V and 5V supplies.

Characteristic	Symbol	Min	Max	Unit
Input Voltage Range	V_{IN}	0	5.5	V
Ambient Temperature Range	T_A	-40	125	°C
High Level Input Voltage for MOSFET to be on	V_{IH}	3	5.5	V
Low Level Input Voltage for MOSFET to be off	V_{IL}	0	0.7	V
Peripheral Supply Voltage (voltage to which load is referred)	V_P	0	16	V

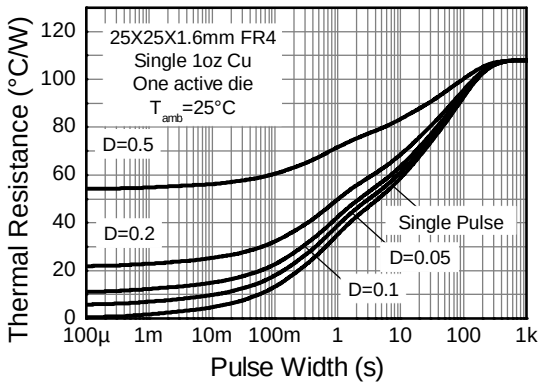
Thermal Characteristics



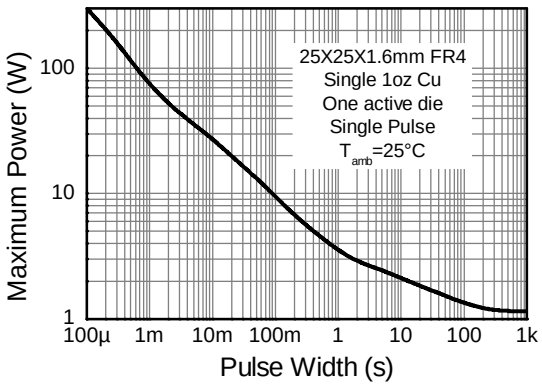
Safe Operating Area



Derating Curve



Transient Thermal Impedance



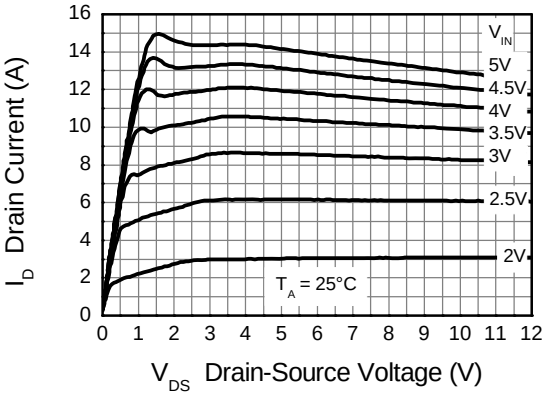
Pulse Power Dissipation

Electrical Characteristics @T_A = 25°C unless otherwise specified

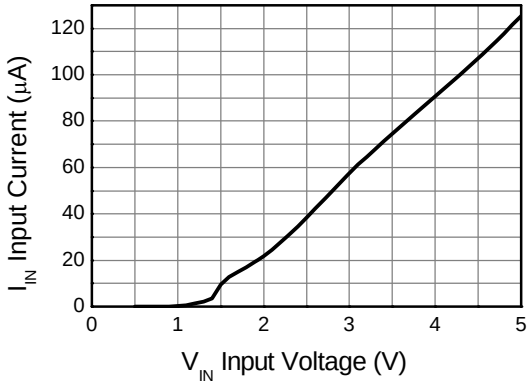
Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Static Characteristics						
Drain-Source Clamp Voltage	$V_{DS(AZ)}$	60	65	70	V	I_D = 10mA
Off State Drain Current	I_{DSS}	-	-	1	µA	V_{DS} = 12V, V_{IN} = 0V
		-	-	2		V_{DS} = 36V, V_{IN} = 0V
Input Threshold Voltage	$V_{IN(th)}$	0.7	1	1.5	V	V_{DS} = V_{GS}, I_D = 1mA
Input Current	I_{IN}	-	60	100	µA	V_{IN} = +3V
		-	120	200		V_{IN} = +5V
Input Current while Over Temperature Active	-	-	-	400	µA	V_{IN} = +5V
Static Drain-Source On-State Resistance	$R_{DS(on)}$	-	85	125	mΩ	V_{IN} = +3V, I_D = 1A
		-	75	100		V_{IN} = +5V, I_D = 1A
Continuous Drain Current (Notes 4 & 8)	I_D	2.0	-	-	A	V_{IN} = 3V; T_A = 25°C
		2.2	-	-		V_{IN} = 5V; T_A = 25°C
Continuous Drain Current (Notes 4 & 7)		2.6	-	-		V_{IN} = 3V; T_A = 25°C
		2.8	-	-		V_{IN} = 5V; T_A = 25°C
Current Limit (Note 10)	$I_{D(LIM)}$	4	8	-	A	V_{IN} = +3V
		6	13	-		V_{IN} = +5V
Dynamic Characteristics						
Turn On Delay Time	$t_{d(on)}$	-	8.6	-	µs	V_{DD} = 12V, I_D = 1A, V_{GS} = 5V
Rise Time	t_r	-	18	-	µs	
Turn Off Delay Time	$t_{d(off)}$	-	34	-	µs	
Fall Time	f_f	-	15	-	µs	
Over-Temperature Protection						
Thermal Overload Trip Temperature (Note 11)	T_{JT}	150	175	-	°C	-
Thermal Hysteresis (Note 11)	f_f	-	10	-	°C	-

Notes:
10. The drain current is restricted only when the device is in saturation (see graph 'typical output characteristic'). This allows the device to be used in the fully on state without interference from the current limit. The device is fully protected at all drain currents, as the low power dissipation generated outside saturation makes current limit unnecessary.
11. Over-temperature protection is designed to prevent device destruction under fault conditions. Fault conditions are considered as "outside" normal operating range, so this part is not designed to withstand over-temperature for extended periods..

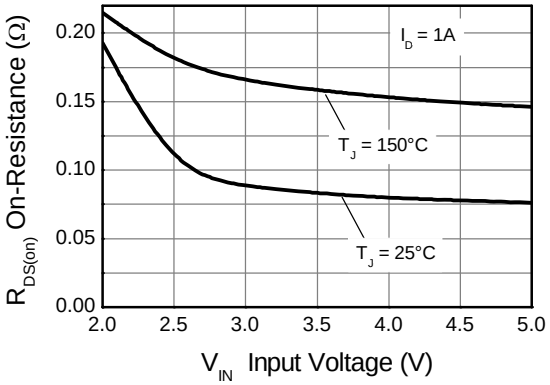
Typical Characteristics



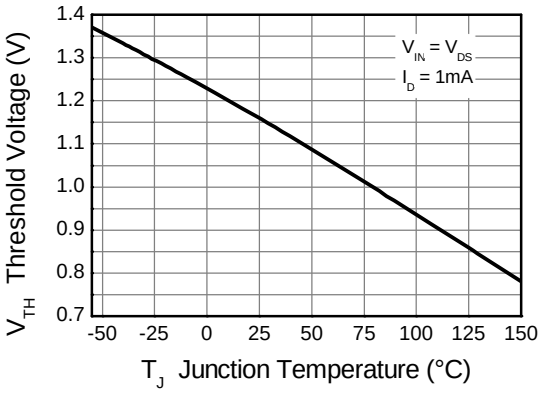
Typical Output Characteristic



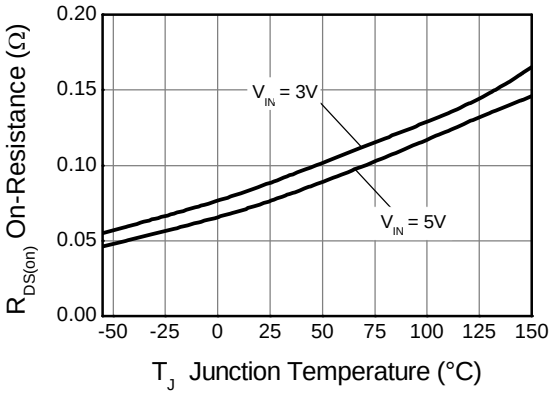
Input Current vs Input Voltage



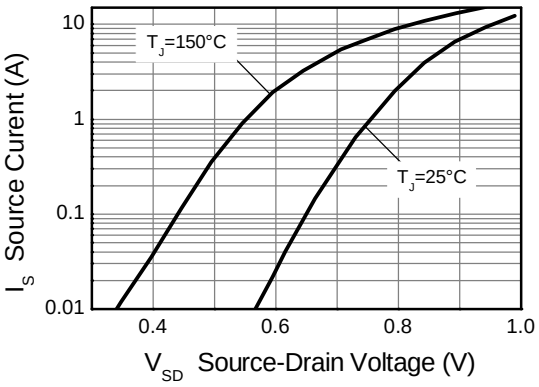
On-Resistance vs Input Voltage



Threshold Voltage vs Temperature

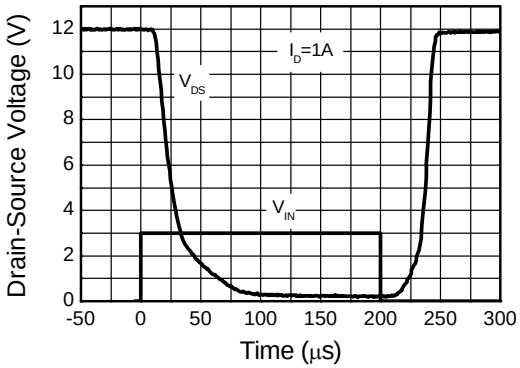


On-Resistance vs Temperature

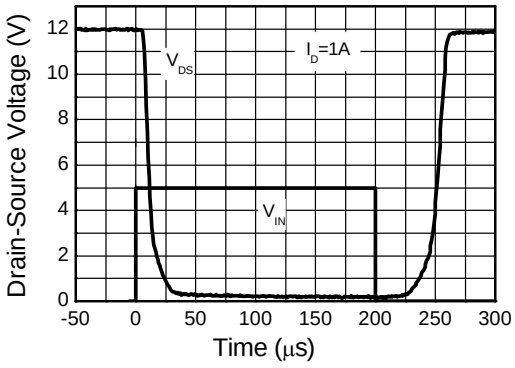


Reverse Diode Characteristic

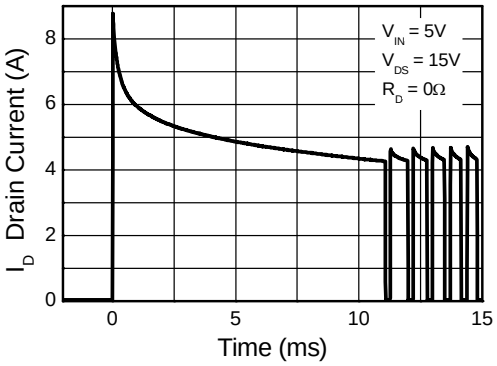
Typical Characteristics - Continued



Switching Speed

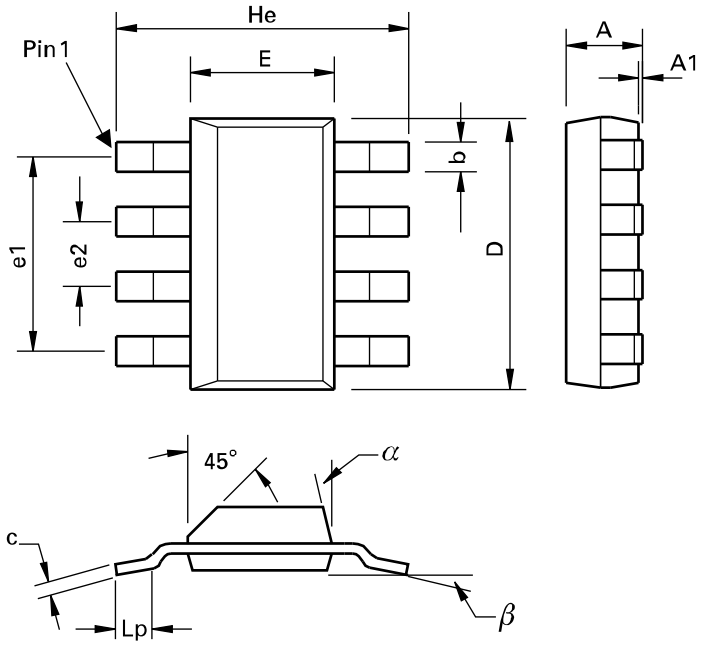


Switching Speed



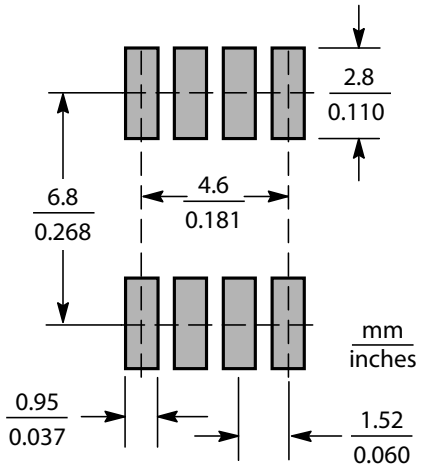
Typical Short Circuit Protection

Package Outline Dimensions



DIM	Millimeters			Inches			DIM	Millimeters			Inches		
	Min	Max	Typ.	Min	Max	Typ.		Min	Max	Typ.	Min	Max	Typ.
A	-	1.7	-	-	0.067	-	e1	-	-	4.59	-	-	0.1807
A1	0.02	0.1	-	0.008	0.004	-	e2	-	-	1.53	-	-	0.0602
b	-	-	0.7	-	-	0.0275	He	6.7	7.3	-	0.264	0.287	-
c	0.24	0.32	-	0.009	0.013	-	Lp	0.9	-	-	0.035	-	-
D	6.3	6.7	-	0.248	0.264	-	α	-	15°	-	-	15°	-
E	3.3	3.7	-	0.130	0.145	-	β	-	-	10°	-	-	10°

Suggested Pad Layout



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