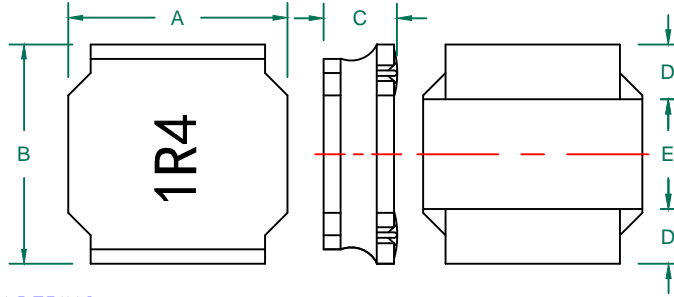


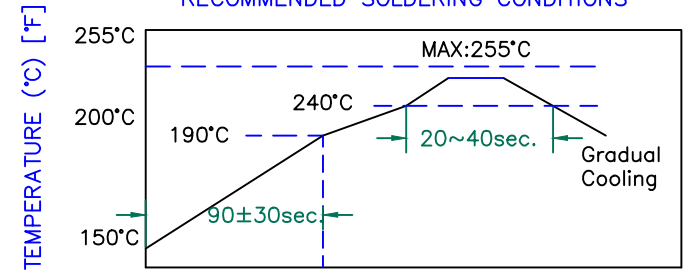
TYS80401R4N-10

PHYSICAL DIMENSIONS:

A	8.00	±	0.30
B	8.00	±	0.30
C	4.00	+ / -	0.20 / 0.30
D	2.45	±	0.30
E	3.10	±	0.30



RECOMMENDED SOLDERING CONDITIONS



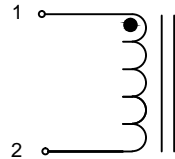
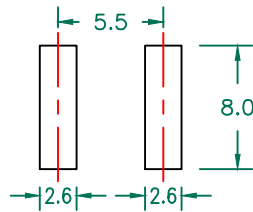
ELECTRICAL SPECIFICATION

	Min	Nom	Max
INDUCTANCE (uH) L @ 100 KHz/1V ± 30%	0.98	1.40	1.82
DCR (Ω)		0.010	0.0130

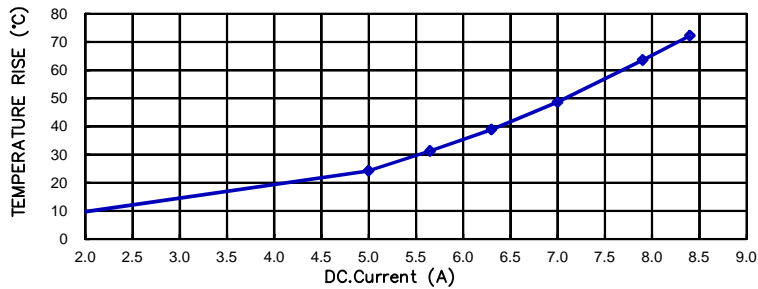
Saturation Current(A)	8.15
SRF (MHz)	67
Temperature Rise Current (A)	5.65

RoHS

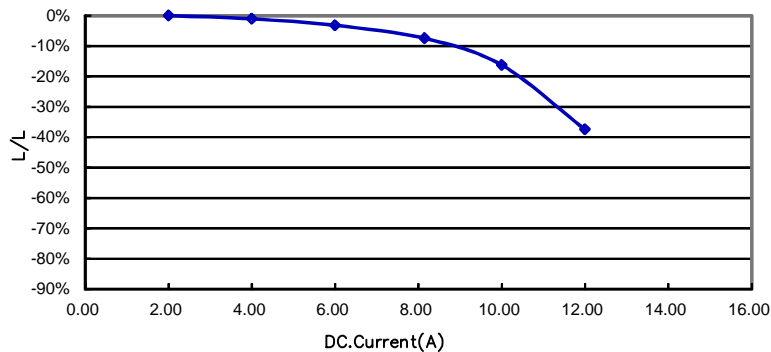
LAND PATTERNS FOR REFLOW SOLDERING



CHARACTERISTICS OF TEMPERATURE RISE



CURRENT VS INDUCTANCE DROP IN RATES



NOTES: UNLESS OTHERWISE SPECIFIED

- OPERATING TEMPERATURE RANGE: -40°C TO +125°C (INCLUDING SELF-HEATING) .
- STORAGE TEMPERATURE RANGE (PACKAGING CONDITIONS): -10°C TO +40°C AND RH 70% (MAX.)
- UNLESS OTHERWISE SPECIFIED, THE STANDARD ATMOSPHERIC CONDITIONS FOR MEASUREMENT/TEST AS:
 - AMBIENT TEMPERATURE: 20±15°C.
 - RELATIVE HUMIDITY: 65%±20%.
- DEFINITION OF SATURATION CURRENT (ISAT): DC CURRENT AT WHICH THE INDUCTANCE DROPS ≤30% FROM ITS VALUE WITHOUT CURRENT.
- DEFINITION OF TEMPERATURE RISE CURRENT (IRMS): DC CURRENT THAT CAUSES THE TEMPERATURE RISE (ΔT ≤40°C) FROM 20°C AMBIENT.

DIMENSIONS ARE IN mm .				This print is the property of Laird Tech. and is loaned in confidence subject to return upon request and with the understanding that no copies shall be made without the written consent of Laird Tech. All rights to design or invention are reserved.			
				Laird			
PROJECT/PART NUMBER:				REV	PART TYPE:	DRAWN BY:	
C	CHANGE DIMENSIONS	05/27/16	QUI	TYS80401R4N-10	C	POWER INDUCTOR	QUI
B	CHANGE TEMP. FROM -25°C~+125°C	12/26/12	QUI	DATE: 06/01/12	SCALE: NTS	SHEET:	
A	ORIGINAL DRAFT	06/01/12	QUI	CAD #	TOOL #	1 of 1	
REV	DESCRIPTION	DATE	INT	TYS80401R4N-10-C			