X7R Dielectric, 6.3 – 250 VDC (Automotive Grade)



Overview

KEMET's Automotive Grade Series surface mount capacitors in X7R dielectric are suited for a variety of applications requiring proven, reliable performance in harsh environments. Whether under-hood or in-cabin, these devices emphasize the vital and robust nature of capacitors required for mission and safety critical automotive circuits. Stricter testing protocol and inspection criteria have been established for automotive grade products in recognition of potentially harsh environmental conditions. KEMET automotive grade series capacitors meet the demanding Automotive Electronics Council's AEC-Q200 qualification requirements.

X7R dielectric features a 125°C maximum operating temperature and is considered "temperature stable," The Electronics Industries Alliance (EIA) characterizes X7R dielectric as a Class II material. Components of this classification are fixed, ceramic dielectric capacitors suited for bypass and decoupling applications or for frequency discriminating circuits where Q and stability of capacitance characteristics are not critical. X7R exhibits a predictable change in capacitance with respect to time and voltage and boasts a minimal change in capacitance with reference to ambient temperature. Capacitance change is limited to ±15% from -55°C to +125°C.

Benefits

- · AEC-Q200 automotive qualified
- -55°C to +125°C operating temperature range
- · Lead (Pb)-free, RoHS and REACH compliant
- Temperature stable dielectric
- EIA 0402, 0603, 0805, 1206, 1210, 1812, and 2220 case sizes
- DC voltage ratings of 6.3 V, 10 V, 16 V, 25 V, 50 V, 100 V, 200 V, and 250 V
- Capacitance offerings ranging from 10 pF to 22 μF
- Available capacitance tolerances of ±5%, ±10%, and ±20%
- · Non-polar device, minimizing installation concerns
- 100% pure matte tin-plated termination finish allowing for excellent solderability



Ordering Information

C	0805	C	225	M	4	R	A	С	AUT0
Ceramic	Case Size (L" x W")	Specification/ Series	Capacitance Code (pF)	Capacitance Tolerance	Rated Voltage (VDC)	Dielectric	Failure Rate/Design	Termination Finish ¹	Packaging/Grade (C-Spec)
	0402 0603 0805 1206 1210 1812 2220	C = Standard	Two significant digits + number of zeros.	J = ±5% K = ±10% M = ±20%	9 = 6.3 8 = 10 4 = 16 3 = 25 5 = 50 1 = 100 2 = 200 A = 250	R = X7R	A = N/A	C = 100% Matte Sn	See "Packaging C-Spec Ordering Options Table" below

¹ Additional termination finish options may be available. Contact KEMET for details



Packaging C-Spec Ordering Options Table

Packaging Type ¹	Packaging/Grade Ordering Code (C-Spec) ³
7" Reel	AUTO
13" Reel/Unmarked	AUTO7411 (EIA 0603 and smaller case sizes) AUTO7210 (EIA 0805 and larger case sizes)
7" Reel/Unmarked/2 mm pitch ²	3190
13" Reel/Unmarked/2 mm pitch ²	3191

¹ Reeling tape options (Paper or Plastic) are dependent on capacitor case size (L" x W") and thickness dimension. See "Chip Thickness/Tape & Reel Packaging Quantities" and "Tape & Reel Packaging Information".

Benefits cont'd

- Capacitance offerings ranging from 10 pF to 22 μF
- Available capacitance tolerances of ±5%, ±10%, and ±20%
- · Non-polar device, minimizing installation concerns
- 100% pure matte tin-plated termination finish allowing for excellent solderability

Applications

Typical applications include decoupling, bypass, filtering and transient voltage suppression.

Qualification/Certification

Automotive Grade products meet or exceed the requirements outlined by the Automotive Electronics Council. Details regarding test methods and conditions are referenced in document AEC-Q200, Stress Test Qualification for Passive Components. For additional information regarding the Automotive Electronics Council and AEC-Q200, please visit their website at www.aecouncil.com.

Environmental Compliance

Lead (Pb)-free, RoHS, and REACH compliant without exemptions.

² The 2 mm pitch option allows for double the packaging quantity of capacitors on a given reel size. This option is limited to EIA 0603 (1608 metric) case size devices. For more information regarding 2 mm pitch option see "Tape & Reel Packaging Information".

³ All Automotive packaging C-Specs listed exclude the option to laser mark components. Please contact KEMET if you require a laser marked option. For more information see "Capacitor Marking".

³ For additional Information regarding "AUTO" C-Spec options, see "Automotive C-Spec Information".



Automotive C-Spec Information

KEMET Automotive Grade products meet or exceed the requirements outlined by the Automotive Electronics Council. Details regarding test methods and conditions are referenced in document AEC-Q200, Stress Test Qualification for Passive Components. These products are supported by a Product Change Notification (PCN) and Production Part Approval Process warrant (PPAP).

Automotive products offered through our distribution channel have been assigned an inclusive ordering code C-Spec, "AUTO". This C-Spec was developed in order to better serve small and medium sized companies that prefer an automotive grade component without the requirement to submit a customer Source Controlled Drawing (SCD) or specification for review by a KEMET engineering specialist. This C-Spec is therefore not intended for use by KEMET's OEM Automotive customers and are not granted the same "privileges" as other automotive C-Specs. Customer PCN approval and PPAP request levels are limited (see details below).

Product Change Notification (PCN)

The KEMET Product Change Notification system is used to communicate primarily the following types of changes:

- Product/process changes that affect product form, fit, function, and/or reliability
- Changes in manufacturing site
- Product obsolescence

KEMET Automotive	Customer Notifica	tion due to:	Days prior to
C-Spec	Process/Product change	Obsolescence*	implementation
KEMET assigned ¹	Yes (with approval and sign off)	Yes	180 days Minimum
AUT0	Yes (without approval)	Yes	90 days Minimum

¹ KEMET assigned C-Specs require the submittal of a customer SCD or customer specification for review. For additional information contact KEMET.

Production Part Approval Process (PPAP)

The purpose of the Production Part Approval Process is:

- To ensure that supplier can meet the manufacturability and quality requirements for the purchased parts.
- To provide the evidence that all customer engineering design record and specification requirements are properly understood and

fulfilled by the manufacturing organization.

• To demonstrate that the established manufacturing process has the potential to produce the part

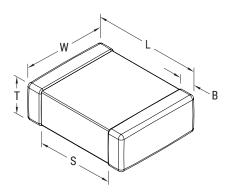
KEMET Automotive	I	PPAP (Product	Part Approval	Process) Leve	I
C-Spec	1	2	3	4	5
KEMET assigned ¹	•	•	•	•	•
AUTO	0		0		

¹ KEMET assigned C-Specs require the submittal of a customer SCD or customer specification for review. For additional information contact KEMET.

- Part Number specific PPAP available
- Product family PPAP only



Dimensions - Millimeters (Inches)



EIA Size Code	Metric Size Code	L Length	W Width	T Thickness	B Bandwidth	S Separation Minimum	Mounting Technique
0402	1005	1.00 (0.040) ±0.05 (0.002)	0.50 (0.020) ±0.05 (0.002)		0.30 (0.012) ±0.10 (0.004)	0.30 (0.012)	Solder Reflow Only
0603	1608	1.60 (0.063) ±0.15 (0.006)	0.80 (0.032) ±0.15 (0.006)		0.35 (0.014) ±0.15 (0.006)	0.70 (0.028)	
0805	2012	2.00 (0.079) ±0.20 (0.008)	1.25 (0.049) ±0.20 (0.008)		0.50 (0.02) ±0.25 (0.010)	0.75 (0.030)	Solder Wave or Solder Reflow
1206	3216	3.20 (0.126) ±0.20 (0.008)	1.60 (0.063) ±0.20 (0.008)	See Table 2 for Thickness	0.50 (0.02) ±0.25 (0.010)		
1210¹	3225	3.20 (0.126) ±0.20 (0.008)	2.50 (0.098) ±0.20 (0.008)		0.50 (0.02) ±0.25 (0.010)	NI/A	
1812	4532	4.50 (0.177) ±0.30 (0.012)	3.20 (0.126) ±0.30 (0.012)		0.60 (0.024) ±0.35 (0.014)	N/A	Solder Reflow Only
2220	5650	5.70 (0.224) ±0.40 (0.016)	5.00 (0.197) ±0.40 (0.016)		0.60 (0.024) ±0.35 (0.014)		

¹ For capacitance values ≥ 4.7 μF add 0.02 (0.001) to the width tolerance dimension and 0.10 (0.004) to the length tolerance dimension.



Electrical Parameters/Characteristics

Item	Parameters/Characteristics
Operating Temperature Range	-55°C to +125°C
Capacitance Change with Reference to +25°C and 0 Vdc Applied (TCC)	±15%
¹ Aging Rate (Maximum % Capacitance Loss/Decade Hour)	3.0%
² Dielectric Withstanding Voltage (DWV)	250% of rated voltage (5±1 seconds and charge/discharge not exceeding 50mA)
³ Dissipation Factor (DF) Maximum Limit at 25°C	See Dissipation Factor Limit Table
⁴ Insulation Resistance (IR) Minimum Limit at 25°C	See Insulation Resistance Limit Table (Rated voltage applied for 120±5 seconds at 25°C)

¹ 1Regarding Aging Rate: Capacitance measurements (including tolerance) are indexed to a referee time of 1,000 hours.

1kHz ± 50Hz and 1.0 ± 0.2 Vrms if capacitance ≤10μF

120Hz \pm 10Hz and 0.5 \pm 0.1 Vrms if capacitance >10 μ F

Note: When measuring capacitance it is important to ensure the set voltage level is held constant. The HP4284 and Agilent E4980 have a feature known as Automatic Level Control (ALC). The ALC feature should be switched to "ON".

²DWV is the voltage a capacitor can withstand (survive) for a short period of time. It exceeds the nominal and continuous working voltage of the capacitor.

³ Capacitance and dissipation factor (DF) measured under the following conditions:

⁴ To obtain IR limit, divide $M\Omega$ - μ F value by the capacitance and compare to $G\Omega$ limit. Select the lower of the two limits.



Post Environmental Limits

High '	Temperature	Life, Biased	Humidity, Mo	oisture Resis	tance
EIA Case Size	Rated DC Voltage	Capacitance	Dissipation Factor (Maximum %)	Capacitance Shift	Insulation Resistance
	< 16	All	7.5		
0402	16 / 25	All	5.0		
	> 25	All	3.0		
	< 16		7.5		
0603	16 / 25	< 1.0µF	5.0		
0003	> 25		3.0		
	All	≥ 1.0µF	20.0		
	< 16	< 4.7μF	7.5		
	< 10	≥ 4.7µF	20.0		
0805	16	< 4.7μF	5.0		
0005	10	≥ 4.7µF	20.0		
	25	All	5.0	±20%	10% of Initial
	> 25	All	3.0	120%	Limit
	< 16	All	7.5		
1206¹	16 / 25	All	5.0		
	> 25	All	3.0		
	< 16	All	7.5		
	16	All	5.0		
1210	25	< 10µF	5.0		
		≥ 10µF	20.0		
	> 25	All	3.0		
	< 16	All	7.5		
1808 - 2225	16 / 25	All	5.0		
	> 25	All	3.0		

 $^{^{1}}$ For Capacitance values 4.7 μ f and 10 μ F DF Post Value is 20% (All Voltages).



Dissipation Factor (DF) Limits Table

EIA Case Size	Rated DC Voltage	Capacitance	Dissipation Factor (Maximum %)				
	< 16		5.0				
0402	16/25	All	3.5				
	> 25		2.5				
	< 16		5.0				
0603	16/25	< 1.0µF	3.5				
0003	> 25		2.5				
	All	≥ 1.0µF	10.0				
	< 16	< 4.7µF	5.0				
	\ 10	≥ 4.7µF	10.0				
0805	16	< 4.7µF	3.5				
0803	10	≥ 4.7µF	10.0				
	25	All	3.5				
	> 25	All	2.5				
	< 16	All	5.0				
1206¹	16/25	All	3.5				
	> 25	All	2.5				
	< 16	All	5.0				
	16	All	3.5				
1210	0.5	< 10µF	3.5				
	25	≥ 10µF	10.0				
	> 25	All	2.5				
	< 16		5.0				
1808 - 2225	16/25	All	3.5				
	> 25		2.5				

 $^{^1}For$ Capacitance values 4.7 μf and 10 μF DF Value is 10% (All Voltages).

Insulation Resistance Limit Table

EIA Case Size	1,000 Megohm Microfarads or 100 GΩ	500 Megohm Microfarads or 10 GΩ	100 Megohm Microfarads or 10 GΩ
0402	< 0.012 μF	≥ 0.012 µF	N/A
0603	< 0.047 μF	≥ 0.047 µf < 0.47 µf	≥ 0.47 µf
0805	< 0.15 μF	≥ 0.15 µF < 2.2 µf	≥ 2.2 µf
1206	< 0.47 μF	≥ 0.47 µF < 4.7 µf	≥ 4.7 µf
1210	< 0.39 μF	≥ 0.39 µF < 10 µf	≥ 10 µf
1808	ALL	N/A	N/A
1812	< 2.2 μF	≥ 2.2 µF	N/A
1825	ALL	N/A	N/A
2220	< 10 μF	≥ 10 µF	N/A
2225	ALL	N/A	N/A



Table 1A - Capacitance Range/Selection Waterfall (0402 - 1206 Case Sizes)

			se S Serio			CO	40	2C				CO	60	3C					(C08	050	-						C12	:06	C		
Capacitance	Cap	Volt	tage C	ode	9	8	4	3	5	9	8	4	3	5	1	2	9	8	4	3	5	1	2	A	9	8	4	3	5	1	2	Α
Oapacitance	Code		ed Vol (VDC)	•	6.3	10	16	25	20	6.3	10	16	25	20	100	200	6.3	10	16	25	20	100	200	250	6.3	10	16	25	20	100	200	250
			oacita oleran					Pro	duct	Ava	ilabi	lity	and (Chip	Thic	kne	ss C	ode	s - S	See T	able	2 fo	r Ch	ip T	hick	ness	s Din	nens	ions	;		
10 - 91 pF*	100 - 910*	J	K	М	ВВ	ВВ	ВВ	ВВ	ВВ	CF	DN		DN		DN	DN	DN		EB													
100 - 150 pF** 180 - 820 pF**	101 - 151** 181 - 820**	J	K	M	BB BB	BB BB	BB BB	BB BB	BB BB	CF CF	DN DN	DN	EB EB																			
1,000 pF	102	J	K	M	BB	BB	BB	BB	BB	CF	DN	EB																				
1,200 pF	122	J	K	М	ВВ	ВВ	ВВ	ВВ	ВВ	CF	DN	EB																				
1,500 pF	152	J	K	М	ВВ	ВВ	BB	BB	BB	CF	DN	EB																				
1,800 pF	182 222	J	K	M	BB BB	BB BB	BB BB	BB BB	BB BB	CF CF	DN DN	DN DN	DN DN	DN	DN DN	DN DN	DN DN	DN DN	EB EB													
2,200 pF 2,700 pF	272	J	K	M	BB	BB	BB	BB	BB	CF	DN	DN	DN	DN DN	DN	DN	DN	DN	EB													
3,300 pF	332	J	K	М	ВВ	BB	BB	BB	BB	CF	DN	EB																				
3,900 pF	392	J	K	М	BB	BB	BB	BB	BB	CF	DN	EB																				
4,700 pF	472	J	K	М	ВВ	ВВ	ВВ	ВВ	ВВ	CF	DN	EB																				
5,600 pF	562	J	K	М	ВВ	ВВ	BB	BB	BB	CF	DN	EB																				
6,800 pF	682	J	K	М	ВВ	BB	BB	BB	BB	CF	DN	EB																				
8,200 pF	822	J	K	M	BB BB	BB BB	BB BB	BB BB	BB	CF	CF	CF	CF CF	CF	CF	CF CF	DN DN	DN DN	DN	DN	DN DN	DN DN	DN DN	DN	EB	EB	EB	EB	EB	EB	EB EB	EB EB
10,000 pF 12,000 pF	103 123	J	K	M	ВВ	BB	BB	BB	BB BB	CF CF	CF CF	CF CF	CF	CF CF	CF CF	UF	DN	DN	DN DN	DN DN	DN	DN	DN	DN DN	EB EB	EB EB	EB EB	EB EB	EB EB	EB EB	EB	EB
15,000 pF	153	J	K	M	BB	BB	BB	BB	BB	CF	CF	CF	CF	CF	CF		DN	DN	DN	DN	DN	DP	DN	DN	EB							
18,000 pF	183	Ĵ	K	М	BB	BB	BB	BB	BB	CF	CF	CF	CF	CF	CF		DN	DN	DN	DN	DN	DP	DN	DN	EB							
22,000 pF	223	J	K	М	ВВ	ВВ	ВВ	ВВ	ВВ	CF	CF	CF	CF	CF	CF		DN	DN	DN	DN	DN	DP	DN	DN	ЕВ	EB						
27,000 pF	273	J	K	М	ВВ	ВВ	ВВ	BB		CF	CF	CF	CF	CF	CF		DN	DN	DN	DN	DN	DP	DE		ЕВ	EB						
33,000 pF	333	J	K	М	ВВ	ВВ	ВВ	ВВ		CF	CF	CF	CF	CF	CF		DN	DN	DN	DN	DN	DP	DE		EB							
39,000 pF	393	J	K	М	BB	BB	BB	BB		CF	CF	CF	CF	CF	CF		DN	DN	DN	DN	DN	DP	DE		EB	EB	EB	EB	EB	EC	EB	EB
47,000 pF 56,000 pF	473 563	J	K	M	BB BB	BB BB	BB BB	BB		CF CF	CF CF	CF CF	CF CF	CF CF	CF		DN DP	DN DP	DN DP	DN DP	DN DP	DE DE	DG DG		EB EB	EB EB	EB EB	EB EB	EB EB	EC EB	ED ED	ED ED
68,000 pF	683	J	K	M	BB	BB	BB			CF	CF	CF	CF	CF			DP	DP	DP	DP	DP	DE	טט		EB	EB	EB	EB	EB	EB	ED	ED
82,000 pF	823	Ĵ	K	М	ВВ	BB	BB			CF	CF	CF	CF	CF			DP	DP	DP	DP	DP	DE			EB	EB	EB	EB	EB	EB	ED	ED
0.10 μF	104	J	K	М	ВВ	ВВ	ВВ			CF	CF	CF	CF	CF			DN	DN	DN	DN	DN	DE			ЕВ	EB	EB	EB	EB	EB	EM	EM
0.12 μF	124	J	K	М						CF	CF	CF	CF	CF			DN	DN	DN	DN	DP	DG			EC	EC	EC	EC	EC	EC	EG	
0.15 μF	154	J	K	М						CF	CF	CF	CF	CF			DN	DN	DN	DN	DP	DG			EC	EC	EC	EC	EC	EC	EG	
0.18 μF	184	J	K	М						CF	CF	CF	CF				DN	DN	DN	DN	DP	DG			EC	EC	EC	EC	EC	EC		
0.22 μF 0.27 μF	224 274	J	K	M	ł					CF CF	CF CF	CF CF	CF				DN DP	DN DP	DN DP	DN DP	DP DP	DG			EC EB	EC EB	EC EB	EC EB	EC EC	EC EM		
0.27 μF 0.33 μF	334	J	K	M						CF	CF	CF					DP	DP	DP	DP	DP				EB	EB	EB	EB	EC	EG		
0.39 μF	394	J	K	M						CF	CF	CF					DG	DG	DG	DG	DE				EB	EB	EB	EB	EC	EG		
0.47 μF	474	J	K	М						CF	CF	CF					DP	DP	DP	DP	DE				EC	EC	EC	EC	EC	EG		
0.56 μF	564	J	K	М													DP	DP	DP	DG	DH				ED	ED	ED	ED	EC			
0.68 μF	684	J	K	М													DP	DP	DP	DG	DH				EE	EE	EE	EE	ED			
0.82 μF	824	J	K	M													DP	DP	DP	DG					EF	EF	EF	EF	ED			
1.0 μF 1.2 μF	105 125	J	K	M													DP DE		DP DE	DG					EF ED	EF ED	EF ED		ED EH			
1.5 μF	155	J	K	M														DG							EF	EF			EH			
1.8 μF	185	J	K	M														DG							ED	ED		EF				
2.2 μF	225	J	K	М														DG							ED	ED	ED	EF				
2.7 μF	275	J	K	М																					EN	EN		EH				
3.3 µF	335	J	K	M																					ED	ED		EH				
3.9 μF	395	J Rate	K ed Vol	M tage		_			_		_			_	•	-		_			_	_		-	EF m	EF		EH	_	-	-	-
			(VDC)		6.3	2	16	25	20	6.3	2	16	25	20	100	200	6.3	2	9	25	20	100	200	250	6.3	2	16	25	20	5	200	250
Capacitance	Cap Code	Volt	tage C	ode	9	8	4	3	5	9	8	4	3	5	1	2	9	8	4	3	5	1	2	A	9	8	4	3	5	1	2	A
	Case Size /Series				C	0402	2C				C	0603	3C						C08	05C							C12	06C				

^{*}Capacitance range includes E24 decade values only. (i.e., 10, 11, 12, 13, 15, 16, 18, 20, 22, 24, 27, 30, 33, 36, 39, 43, 47, 51, 56, 62, 68, 75, 82, and 91)

^{**}Capacitance range includes E12 decade values only. (i.e., 10, 12, 15, 18, 22, 27, 33, 39, 47, 56, 68, and 82)



Table 1A - Capacitance Range/Selection Waterfall (0402 - 1206 Case Sizes) cont'd

				e S eri	Size es		C)40	2C				C)60	3C					(208	050	C					(C12	060)		
Capacitance	Cap	٧	olta	age (Code	9	8	4	3	5	9	8	4	3	5	1	2	9	8	4	3	5	1	2	Α	9	8	4	3	5	1	2	Α
Capacitance	Code	Ra		d Vo VDC	ltage)	6.3	9	91	25	20	6.3	10	16	25	20	100	200	6.3	10	16	25	20	100	200	250	6.3	10	16	25	20	100	200	250
		C		acita Ierai	nce nce				Pro	duct	Ava	ilabi	lity	and	Chip	Thi	kne	ss C	ode	s - S	ee 1	able	2 fc	r Ch	ip T	hick	ness	Dim	iens	ions			
4.7 μF	475	Γ,	jΠ	K	М																					EF	EF	EF	EH				
5.6 μF	565	١,	J	K	M						l							l								EH	EH	EH					
6.8 µF	685	ļ,	J	K	M						l															EΗ	EH	EH					
8.2 μF	825	١,	J	K	M																					EH	EH	EH					
10 μF	106	١,	J	K	М						l							İ								EH	EH	EH					
		Ra		d Vo VDC	ltage)	6.3	10	16	25	20	6.3	10	16	25	20	100	200	6.3	10	16	25	20	100	200	250	6.3	10	16	25	20	100	200	250
Capacitance	Cap Code	٧	olta	age (Code	9	8	4	3	5	9	8	4	3	5	1	2	9	8	4	3	5	1	2	A	9	8	4	3	5	1	2	Α
·				se S Serie			С	040	2C				С	0603	3C						COS	05C							C12	06C			

^{*}Capacitance range includes E24 decade values only. (i.e., 10, 11, 12, 13, 15, 16, 18, 20, 22, 24, 27, 30, 33, 36, 39, 43, 47, 51, 56, 62, 68, 75, 82, and 91)

Table 1B - Capacitance Range/Selection Waterfall (1210 - 2220 Case Sizes)

			se S Seri	Size es				C12	100	;			C	1808	ВС		C	1812	2C			C18	250	;		C2	2220	OC	
Consoitones	Cap	Volt	age (Code	9	8	4	3	5	1	2	Α	5	1	2	3	5	1	2	A	5	1	2	Α	3	5	1	2	Α
Capacitance	Code		d Vol	ltage)	6.3	10	16	25	20	100	200	250	20	100	200	25	20	100	200	250	20	100	200	250	25	20	100	200	250
			acita Ieran				P	rodu	ct Av	ailab	ility	and (Chip	Thic	knes	s Co	des -	See	Tabl	e 2 f	or Ch	ip Tl	nickn	ess	Dime	nsio	ıs		
10 - 91 pF*	100 - 910*	J	K	М	FB	FB	FB	FB	FB	FB	FB																		
100 - 270 pF**	101 - 391**	J	K	М	FB	FB	FB	FB	FB	FB	FB					l									l				
330 pF	331	J	K	M	FB	FB	FB	FB	FB	FB	FB		LF	LF	LF	l													
390 pF	391	J	K	M	FB	FB	FB	FB	FB	FB	FB		LF	LF	LF	l													
470 - 820 pF**	471 - 821**	J	K	M	FB	FB	FB	FB	FB	FB	FB		LF	LF	LF	GB	GB	GB	GB										
1,000 pF	102	J	K	M	FB	FB	FB	FB	FB	FB	FB		LF	LF	LF	GB	GB	GB	GB										
1,200 pF	122	J	K	M	FB	FB	FB	FB	FB	FB	FB		LF	LF	LF	GB	GB	GB	GB										
1,500 pF	152	J	K	M	FB	FB	FB	FB	FB	FB	FE		LF	LF	LF	GB	GB	GB	GB										
1,800 pF	182	J	K	M	FB	FB	FB	FB	FB	FB	FE		LF	LF	LF	GB	GB	GB	GB										
2,200 pF	222	J	K	M	FB	FB	FB	FB	FB	FB	FB	FB	LF	LF	LF	GB	GB	GB	GB										
2,700 pF	272	J	K	M	FB	FB	FB	FB	FB	FB	FB	FB	LF	LF	LF	GB	GB	GB	GB										
3,300 pF	332	J	K	M	FB	FB	FB	FB	FB	FB	FB	FB	LF	LF		GB	GB	GB	GB										
3,900 pF	392	J	K	M	FB	FB	FB	FB	FB	FB	FB	FB	LF	LF		GB	GB	GB	GB										
4,700 pF	472	J	K	M	FB	FB	FB	FB	FB	FB	FB	FB	LD	LD		GB	GB	GB	GD										
5,600 pF	562	J	K	M	FB	FB	FB	FB	FB	FB	FB	FB	LD	LD		GB	GB	GB	GH										
6,800 pF	682	J	K	М	FB	FB	FB	FB	FB	FB	FB	FB	LD	LD		GB	GB	GB	GB	GB					JE	JE	JE		
8,200 pF	822	J	K	М	FB	FB	FB	FB	FB	FB	FB	FB	LD	LD		GB	GB	GB	GB	GB					JE	JE	JE		
10,000 pF	103	J	K	М	FB	FB	FB	FB	FB	FB	FB	FB	LD	LD		GB	GB	GB	GB	GB					JE	JE	JE		
12,000 pF	123	J	K	М	FB	FB	FB	FB	FB	FB	FB	FB	LD	LD		GB	GB	GB	GB	GB					JE	JE	JE		
			d Vol (VDC		6.3	5	16	25	20	100	200	250	20	100	200	25	20	100	200	250	20	100	200	250	25	20	100	200	250
Capacitance	Cap Code	Volt	age (Code	9	8	4	3	5	1	2	A	5	1	2	3	5	1	2	A	5	1	2	A	3	5	1	2	A
		Case Size /Series						C12	10C				С	1808	C		C	1812	C.			C18	25C			С	2220	С	

^{*}Capacitance range includes E24 decade values only. (i.e., 10, 11, 12, 13, 15, 16, 18, 20, 22, 24, 27, 30, 33, 36, 39, 43, 47, 51, 56, 62, 68, 75, 82, and 91)

^{**}Capacitance range includes E12 decade values only. (i.e., 10, 12, 15, 18, 22, 27, 33, 39, 47, 56, 68, and 82)

^{**}Capacitance range includes E12 decade values only. (i.e., 10, 12, 15, 18, 22, 27, 33, 39, 47, 56, 68, and 82)



Table 1B - Capacitance Range/Selection Waterfall (1210 - 2220 Case Sizes) cont'd

	0	/5	se S Seri	es				C12	100	;				1808				1812				C18	250	;			222		
Capacitance	Cap	⊢	tage (9	8	4	3	5	1	2	Α	5	1	2	3	5	1	2	Α	5	1	2	Α	3	5	1	2	Α
	Code		ed Vol (VDC		6.3	2	16	25	20	9	200	250	20	100	200	25	20	100	200	250	20	100	200	250	25	20	100	200	250
			oacita oleran				Р	rodu	ct Av	ailal	ility	and	Chip	Thic	knes	s Cod	des -	See	Tabl	e 2 f	or Ch	ip Th	nickn	ess I	Dime	nsio	15		
15,000 pF	153	J	K	М	FB	FB	FB	FB	FB	FB	FB	FB	LD	LD		GB	GB	GB	GB	GB					JE	JE	JE		
18,000 pF	183	J	K	М	FB	FB	FB	FB	FB	FB	FB	FB	LD	LD		GB	GB	GB	GB	GB					JE	JE	JE		
22,000 pF	223	J	K	М	FB	FB	FB	FB	FB	FB	FB	FB	LD	LD		GB	GB	GB	GB	GB	НВ	НВ	НВ	НВ	JE	JE	JE		
27,000 pF	273	J	K	М	FB	FB	FB	FB	FB	FB	FB	FB	LD	LD		GB	GB	GB	GB	GB	НВ	НВ	НВ	НВ	JE	JE	JE		
33,000 pF	333	J	K	M	FB	FB	FB	FB	FB	FB	FB	FB	LD	LD		GB	GB	GB	GB	GB	НВ	НВ	НВ	HB	JB	JB	JB		
39,000 pF	393	J	K	М	FB	FB	FB	FB	FB	FB	FB	FB	LD	LD		GB	GB	GB	GB	GB	НВ	НВ	НВ	НВ	JB	JB	JB		
47,000 pF	473	J	K	М	FB	FB	FB	FB	FB	FB	FB	FB	LD	LD		GB	GB	GB	GB	GB	НВ	НВ	НВ	HB	JB	JB	JB		
56,000 pF	563	J	K	M	FB	FB	FB	FB	FB	FB	FC	FC	LD	LD		GB	GB	GB	GB	GB	НВ	НВ	НВ	HB	JB	JB	JB		
68,000 pF	683	J	K	M	FB	FB	FB	FB	FB	FB	FC	FC	LD			GB	GB	GB	GB	GB	НВ	НВ	НВ	НВ	JB	JB	JB		
82,000 pF	823	J	K	M	FB	FB	FB	FB	FB	FC	FF	FF	LD			GB	GB	GB	GB	GB	НВ	НВ	НВ	НВ	JB	JB	JC	JC	JC
0.10 μF	104	J	K	M	FB	FB	FB	FB	FB	FD	FG	FG	LD			GB	GB	GB	GB	GB	НВ	НВ	НВ	НВ	JB	JB	JC	JC	JC
0.12 μF	124	J	K	М	FB	FB	FB	FB	FB	FD	FH	FH	LD			GB	GB	GB	GB	GB	НВ	НВ	НВ	НВ	JB	JB	JC	JC	JC
0.15 μF	154	J	K	М	FC	FC	FC	FC	FC	FD	FM	FM	LD			GB	GB	GB	GE	GE	НВ	НВ	НВ	HB	JB	JB	JC	JC	JC
0.18 μF	184	J	K	M	FC	FC	FC	FC	FC	FD	FK	FK	LD			GB	GB	GB	GG	GG	НВ	НВ	НВ	HB	JB	JB	JC	JC	JC
0.22 μF	224	J	K	M	FC	FC	FC	FC	FC	FD	FK	FK				GB	GB	GB	GG	GG	НВ	НВ	НВ	НВ	JB	JB	JC	JC	JC
0.27 μF	274	J	K	М	FC	FC	FC	FC	FC	FD						GB	GB	GG	GG	GG	НВ	НВ	НВ	НВ	JC	JC	JC	JC	JC
0.33 μF	334	J	K	М	FD	FD	FD	FD	FD	FD						GB	GB	GG	GG	GG	НВ	НВ	НВ	НВ	JC	JC	JC	JC	JC
0.39 μF	394	J	K	М	FD	FD	FD	FD	FD	FD			l			GB	GB	GG	GG	GG	HD	HD	HD	HD	JC	JC	JC	JC	JC
0.47 μF	474	J	K	М	FD	FD	FD	FD	FD	FD			l			GB	GB	GG	GJ	GJ	HD	HD	HD	HD	JC	JC	JC	JC	JC
0.56 μF	564	J	K	М	FD	FD	FD	FD	FD	FF			l			GC	GC	GG			HD	HD	HD	HD	JC	JD	JD	JD	JD
0.68 μF	684	J	K	М	FD	FD	FD	FD	FD	FG			İ			GC	GC	GG			HD	HD	HD	HD	JC	JD	JD	JD	JD
0.82 μF	824	J	K	М	FF	FF	FF	FF	FF	FL						GE	GE	GG			HF	HF	HF	HF	JC	JF	JF	JF	JF
1.0 µF	105	J	K	М	FH	FH	FH	FH	FH	FM						GE	GE	GG			HF	HF	HF	HF	JC	JF	JF	JF	JF
1.2 μF	125	J	K	М	FH	FH	FH	FH	FG	FH						GB	GB	GB							JC	JC			
1.5 μF	155	J	K	М	FH	FH	FH	FH	FG	FM						GC	GC	GC							JC	JC			
1.8 μF	185	J	K	М	FH	FH	FH	FH	FG	FJ						GE	GE	GE							JD	JD			
2.2 μF	225	J	K	М	FJ	FJ	FJ	FJ	FG	FK						GO	GO	GG							JF	JF			
2.7 μF	275	J	K	M	FE	FE	FE	FG	FH							GJ	GJ	GJ											
3.3 μF	335	J	K	М	FF	FF	FF	FM	FM				l			GL	GL	GL			l				l				
3.9 μF	395	J	K	М	FG	FG	FG	FG	FK				l			l					l				l				
4.7 μF	475	J	K	М	FC	FC	FC	FG	FS							GK	GK				1				JF	JF			
5.6 μF	565	J	K	М	FF	FF	FF	FH																					
6.8 μF	685	J	K	М	FG	FG	FG	FM																					
8.2 µF	825	J	K	М	FH	FH	FH	FK																					
10 μF	106	J	K	М	FH	FH	FH	FS								GK									JF	JO			
12 µF	126	J	K	М																									
15 μF	156	J	K	М																					J0				
18 μF	186	J	K	М									l								l				l				
22 µF	226	J	K	М	FS	FS																			J0				
			ed Vol (VDC		6.3	2	9	22	20	100	200	250	20	100	200	25	20	100	200	250	20	100	200	250	25	20	100	200	250
Capacitance	Cap Code	Volt	tage (Code	9	8	4	3	5	1	2	Α	5	1	2	3	5	1	2	Α	5	1	2	Α	3	5	1	2	Α
			se S Serie					C12	10C				С	1808	C		C	1812	C.			C18	25C			С	2220	С	

^{*}Capacitance range includes E24 decade values only. (i.e., 10, 11, 12, 13, 15, 16, 18, 20, 22, 24, 27, 30, 33, 36, 39, 43, 47, 51, 56, 62, 68, 75, 82, and 91)

^{**}Capacitance range includes E12 decade values only. (i.e., 10, 12, 15, 18, 22, 27, 33, 39, 47, 56, 68, and 82)



Table 2 - Chip Thickness/Tape & Reel Packaging Quantities

	Case	Thickness ±			Plastic (Quantity
Code	Size ¹	Range (mm)	7" Reel	13" Reel	7" Reel	13" Reel
BB	0402	0.50 ± 0.05	10,000	50,000	0	0
CF	0603	0.80 ± 0.07	4,000	15,000	0	0
DN	0805	0.78 ± 0.10	4,000	15,000	0	0
DP DE	0805 0805	0.90 ± 0.10 1.00 ± 0.10	4,000 0	15,000 0	0 2,500	0 10,000
DG	0805	1.25 ± 0.15	0	0	2,500	10,000
DH	0805	1.25 ± 0.13	0	0	2,500	10,000
EB	1206	0.78 ± 0.10	4,000	10,000	4,000	10,000
EC	1206	0.90 ± 0.10	0	0	4,000	10,000
EN	1206	0.95 ± 0.10	0	0	4,000	10,000
ED	1206	1.00 ± 0.10	0	0	2,500	10,000
EE	1206	1.10 ± 0.10	0	0	2,500	10,000
EF	1206	1.20 ± 0.15	0	0	2,500	10,000
EM	1206 1206	1.25 ± 0.15	0 0	0 0	2,500	10,000
EG EH	1206	1.60 ± 0.15 1.60 ± 0.20	0	0	2,000 2,000	8,000 8,000
FB	1210	0.78 ± 0.10	0	0	4,000	10,000
FC	1210	0.90 ± 0.10	0	0	4,000	10,000
FD	1210	0.95 ± 0.10	Ö	Ö	4,000	10,000
FE	1210	1.00 ± 0.10	0	0	2,500	10,000
FF	1210	1.10 ± 0.10	0	0	2,500	10,000
FG	1210	1.25 ± 0.15	0	0	2,500	10,000
FL	1210	1.40 ± 0.15	0	0	2,000	8,000
FH	1210	1.55 ± 0.15	0	0	2,000	8,000
FM	1210	1.70 ± 0.20	0	0	2,000	8,000
FJ	1210	1.85 ± 0.20	0	0	2,000	8,000
FK FS	1210 1210	2.10 ± 0.20 2.50 ± 0.30	0 0	0 0	2,000 1,000	8,000 4,000
NA NA	1706	0.90 ± 0.10	0	0	4,000	10,000
NC	1706	1.00 ± 0.15	0	0	4,000	10,000
LD	1808	0.90 ± 0.10	0	0	2,500	10,000
LF	1808	1.00 ± 0.15	0	0	2,500	10,000
GB	1812	1.00 ± 0.10	0	0	1,000	4,000
GC	1812	1.10 ± 0.10	0	0	1,000	4,000
GD	1812	1.25 ± 0.15	0	0	1,000	4,000
GE	1812	1.30 ± 0.10	0	0	1,000	4,000
GH	1812	1.40 ± 0.15	0	0	1,000	4,000
GG GK	1812 1812	1.55 ± 0.10 1.60 ± 0.20	0 0	0 0	1,000 1,000	4,000 4,000
GJ	1812	1.70 ± 0.20	0	0	1,000	4,000
GO	1812	2.50 ± 0.20	0	0	500	2,000
HB	1825	1.10 ± 0.15	Ö	Ö	1,000	4,000
HD	1825	1.30 ± 0.15	0	0	1,000	4,000
HF	1825	1.50 ± 0.15	0	0	1,000	4,000
JB	2220	1.00 ± 0.15	0	0	1,000	4,000
JC	2220	1.10 ± 0.15	0	0	1,000	4,000
JD	2220	1.30 ± 0.15	0	0	1,000	4,000
JE	2220	1.40 ± 0.15	0	0	1,000	4,000
JF J0	2220 2220	1.50 ± 0.15 2.40 ± 0.15	0 0	0 0	1,000 500	4,000 2,000
			7" Reel	13" Reel	7" Reel	2,000 13" Reel
Thickness Code	Case Size1	Thickness ± Range (mm)	Paper Q			Quantity

Package quantity based on finished chip thickness specifications.

¹ If ordering using the 2 mm Tape and Reel pitch option, the packaging quantity outlined in the table above will be doubled. This option is limited to EIA 0603 (1608 metric) case size devices. For more information regarding 2 mm pitch option see "Tape & Reel Packaging Information".



Table 3 - Chip Capacitor Land Pattern Design Recommendations per IPC-7351

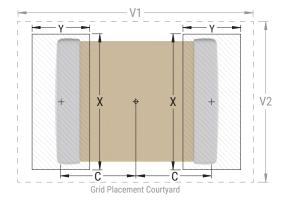
EIA Size Code	Metric Size Code	ı	Density Level A: Maximum (Most) Land Protrusion (mm)				Density Level B: Median (Nominal) Land Protrusion (mm)					Density Level C: Minimum (Least) Land Protrusion (mm)				
Code	Odde	С	Υ	X	V1	V2	С	Y	X	V1	V2	С	Y	X	V1	V2
0402	1005	0.50	0.72	0.72	2.20	1.20	0.45	0.62	0.62	1.90	1.00	0.40	0.52	0.52	1.60	0.80
0603	1608	0.90	1.15	1.10	4.00	2.10	0.80	0.95	1.00	3.10	1.50	0.60	0.75	0.90	2.40	1.20
0805	2012	1.00	1.35	1.55	4.40	2.60	0.90	1.15	1.45	3.50	2.00	0.75	0.95	1.35	2.80	1.70
1206	3216	1.60	1.35	1.90	5.60	2.90	1.50	1.15	1.80	4.70	2.30	1.40	0.95	1.70	4.00	2.00
1210	3225	1.60	1.35	2.80	5.65	3.80	1.50	1.15	2.70	4.70	3.20	1.40	0.95	2.60	4.00	2.90
1210¹	3225	1.50	1.60	2.90	5.60	3.90	1.40	1.40	2.80	4.70	3.30	1.30	1.20	2.70	4.00	3.00
1812	4532	2.15	1.60	3.60	6.90	4.60	2.05	1.40	3.50	6.00	4.00	1.95	1.20	3.40	5.30	3.70
2220	5650	2.75	1.70	5.50	8.20	6.50	2.65	1.50	5.40	7.30	5.90	2.55	1.30	5.30	6.60	5.60

¹ Only for capacitance values ≥ 22 μF

Density Level A: For low-density product applications. Recommended for wave solder applications and provides a wider process window for reflow solder processes. KEMET only recommends wave soldering of EIA 0603, 0805, and 1206 case sizes.

Density Level B: For products with a moderate level of component density. Provides a robust solder attachment condition for reflow solder processes. Density Level C: For high component density product applications. Before adapting the minimum land pattern variations the user should perform qualification testing based on the conditions outlined in IPC Standard 7351 (IPC-7351).

Image below based on Density Level B for an EIA 1210 case size.





Soldering Process

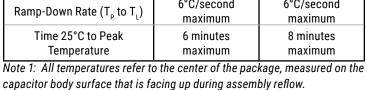
Recommended Soldering Technique:

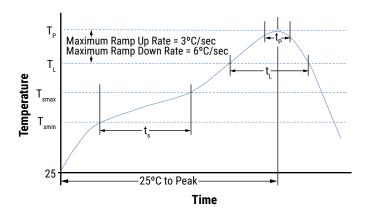
- Solder wave or solder reflow for EIA case sizes 0603, 0805 and 1206
- All other EIA case sizes are limited to solder reflow only

Recommended Reflow Soldering Profile:

KEMET's families of surface mount multilayer ceramic capacitors (SMD MLCCs) are compatible with wave (single or dual), convection, IR or vapor phase reflow techniques. Preheating of these components is recommended to avoid extreme thermal stress. KEMET's recommended profile conditions for convection and IR reflow reflect the profile conditions of the IPC/ J-STD-020 standard for moisture sensitivity testing. These devices can safely withstand a maximum of three reflow passes at these conditions.

Profile Feature	Terminati	ion Finish
1 Tome Teature	SnPb	100% Matte Sn
Preheat/Soak		
Temperature Minimum (T _{Smin})	100°C	150°C
Temperature Maximum (T _{Smax})	150°C	200°C
Time (t_s) from T_{smin} to T_{smax}	60 - 120 seconds	60 - 120 seconds
Ramp-Up Rate (T _L to T _P)	3°C/second maximum	3°C/second maximum
Liquidous Temperature (T_L)	183°C	217°C
Time Above Liquidous (t _L)	60 - 150 seconds	60 - 150 seconds
Peak Temperature (T _P)	235°C	260°C
Time Within 5°C of Maximum Peak Temperature (t _p)	20 seconds maximum	30 seconds maximum
Ramp-Down Rate $(T_P \text{ to } T_L)$	6°C/second maximum	6°C/second maximum
Time 25°C to Peak Temperature	6 minutes maximum	8 minutes maximum



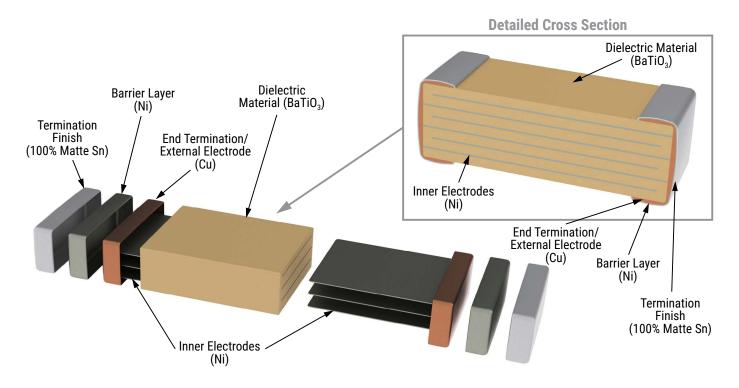


Storage and Handling

Ceramic chip capacitors should be stored in normal working environments. While the chips themselves are quite robust in other environments, solderability will be degraded by exposure to high temperatures, high humidity, corrosive atmospheres, and long term storage. In addition, packaging materials will be degraded by high temperature-reels may soften or warp and tape peel force may increase. KEMET recommends that maximum storage temperature not exceed 40°C and maximum storage humidity not exceed 70% relative humidity. Temperature fluctuations should be minimized to avoid condensation on the parts and atmospheres should be free of chlorine and sulfur bearing compounds. For optimized solderability chip stock should be used promptly, preferably within 1.5 years of receipt.



Construction (Typical)





Capacitor Marking (Optional):

These surface mount multilayer ceramic capacitors are normally supplied unmarked. If required, they can be marked as an extra cost option. Marking is available on most KEMET devices but must be requested using the correct ordering code identifier(s). If this option is requested, two sides of the ceramic body will be laser marked with a "K" to identify KEMET, followed by two characters (per EIA-198 - see table below) to identify the capacitance value. EIA 0603 case size devices are limited to the "K" character only.

Laser marking option is <u>not</u> available on:

- COG, Ultra Stable X8R and Y5V dielectric devices
- EIA 0402 case size devices
- EIA 0603 case size devices with Flexible Termination option.
- KPS Commercial and Automotive Grade stacked devices.

•	X7R dielectric pro	ducts in	ı capaci	tance va	lues ou	tlined
	below					

Marking appears in legible contrast. Illustrated below is an example of an MLCC with laser marking of "KA8", which designates a KEMET device with rated capacitance of 100 µF. Orientation of marking is vendor optional.



EIA Case Size	Metric Size Code	Capacitance
0603	1608	≤ 170 pF
0805	2012	≤ 150 pF
1206	3216	≤ 910 pF
1210	3225	≤ 2,000 pF
1808	4520	≤ 3,900 pF
1812	4532	≤ 6,700 pF
1825	4564	≤ 0.018 µF
2220	5650	≤ 0.027 µF
2225	5664	≤ 0.033 µF



Capacitor Marking (Optional) cont'd

	C	apacita	ance (p	F) For \	/arious	Alpha/	Numera	al Identif	iers	
						Numera				
Alpha	9	0	1	2	3	4	5	6	7	8
Character					Capa	citance	e (pF)			
Α	0.1	10	10	100	1,000	10,000	100,000	1,000,000	10,000,000	100,000,000
В	0.11	1.1	11	110	1,100	11,000	110,000	1,100,000	11,000,000	110,000,000
С	0.12	12	12	120	1,200	12,000	120,000	1,200,000	12,000,000	120,000,000
D	0.13	13	13	130	1,300	13,000	130,000	1,300,000	13,000,000	130,000,000
E	0.15	1 5	15	150	1,500	15,000	150,000	1,500,000	15,000,000	150,000,000
F	0.16	16	16	160	1,600	16,000	160,000	1,600,000	16,000,000	160,000,000
G	0.18	18	18	180	1,800	18,000	180,000	1,800,000	18,000,000	180,000,000
Н	0.2	2 0	20	200	2,000	20,000	200,000	2,000,000	20,000,000	200,000,000
J	0.22	2 2	22	220	2,200	22,000	220,000	2,200,000	22,000,000	220,000,000
К	0.24	2.4	24	240	2,400	24,000	240,000	2,400,000	24,000,000	240,000,000
L	0.27	2.7	27	270	2,700	27,000	270,000	2,700,000	27,000,000	270,000,000
М	0.3	3 0	30	300	3,000	30,000	300,000	3,000,000	30,000,000	300,000,000
N	0.33	3 3	33	330	3,300	33,000	330,000	3,300,000	33,000,000	330,000,000
Р	0.36	3 6	36	360	3,600	36,000	360,000	3,600,000	36,000,000	360,000,000
Q	0.39	3 9	39	390	3,900	39,000	390,000	3,900,000	39,000,000	390,000,000
R	0.43	4 3	43	430	4,300	43,000	430,000	4,300,000	43,000,000	430,000,000
S	0.47	4.7	47	470	4,700	47,000	470,000	4,700,000	47,000,000	470,000,000
T	0.51	5.1	51	510	5,100	51,000	510,000	5,100,000	51,000,000	510,000,000
U	0.56	5 6	56	560	5,600	56,000	560,000	5,600,000	56,000,000	560,000,000
٧	0.62	6 2	62	620	6,200	62,000	620,000	6,200,000	62,000,000	620,000,000
W	0.68	6 8	68	680	6,800	68,000	680,000	6,800,000	68,000,000	680,000,000
Х	0.75	7 5	75	750	7,500	75,000	750,000	7,500,000	75,000,000	750,000,000
Υ	0.82	8 2	82	820	8,200	82,000	820,000	8,200,000	82,000,000	820,000,000
Z	0.91	9.1	91	910	9,100	91,000	910,000	9,100,000	91,000,000	910,000,000
а	0.25	2 5	25	250	2,500	25,000	250,000	2,500,000	25,000,000	250,000,000
b	0.35	3 5	35	350	3,500	35,000	350,000	3,500,000	35,000,000	350,000,000
d	0.4	4 0	40	400	4,000	40,000	400,000	4,000,000	40,000,000	400,000,000
e	0.45	4 5	45	450	4,500	45,000	450,000	4,500,000	45,000,000	450,000,000
f	0.5	5 0	50	500	5,000	50,000	500,000	5,000,000	50,000,000	500,000,000
m	0.6	6 0	60	600	6,000	60,000	600,000	6,000,000	60,000,000	600,000,000
n	0.7	7 0	70	700	7,000	70,000	700,000	7,000,000	70,000,000	700,000,000
t	0.8	8 0	80	800	8,000	80,000	800,000	8,000,000	80,000,000	800,000,000
у	0.9	9 0	90	900	9,000	90,000	900,000	9,000,000	90,000,000	900,000,000



Tape & Reel Packaging Information

KEMET offers multilayer ceramic chip capacitors packaged in 8, 12 and 16 mm tape on 7" and 13" reels in accordance with EIA Standard 481. This packaging system is compatible with all tape-fed automatic pick and place systems. See Table 2 for details on reeling quantities for commercial chips.

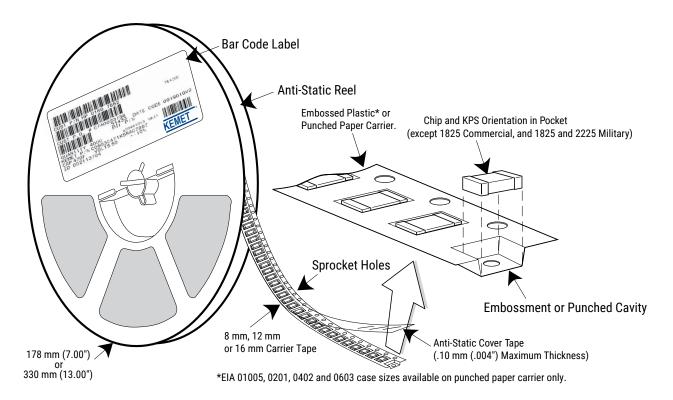


Table 5 - Carrier Tape Configuration, Embossed Plastic & Punched Paper (mm)

	Tape	Embosse	d Plastic	Punche	d Paper	
EIA Case Size	Size	7" Reel	13" Reel	7" Reel	13" Reel	
	(W)*	Pitch	(P ₁)*	Pitch (P₁)*		
01005 - 0402	8			2	2	
0603	8			2/4	2/4 -	
0805	8	4	4	4	4	
1206 - 1210	8	4	4	4	4	
1805 - 1808	12	4	4			
≥ 1812	12	8	8			
KPS 1210	12	8	8			
KPS 1812 & 2220	16	12	12			
Array 0508 & 0612	8	4	4			

^{*}Refer to Figures 1 & 2 for W and P_1 carrier tape reference locations.

New 2 mm Pitch Reel Options*

	Packaging Ordering Code (C-Spec)	Packaging Type/Options
•	C-3190	Automotive grade 7" reel unmarked
	C-3191	Automotive grade 13" reel unmarked
	C-7081	Commercial grade 7" reel unmarked
	C-7082	Commercial grade 13" reel unmarked

^{* 2} mm pitch reel only available for 0603 EIA case size. 2 mm pitch reel for 0805 EIA case size under development.

Benefits of Changing from 4 mm to 2 mm Pitching Spacing

- · Lower placement costs
- Double the parts on each reel results in fewer reel changes and increased efficiency
- · Fewer reels result in lower packaging, shipping and storage costs, reducing waste

^{*}Refer to Tables 6 & 7 for tolerance specifications.



Figure 1 - Embossed (Plastic) Carrier Tape Dimensions

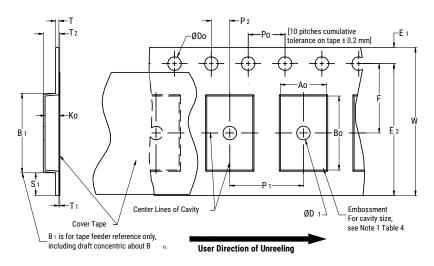


Table 6 – Embossed (Plastic) Carrier Tape Dimensions

Metric will govern

		(Constant Dim	ensions – Mi	llimeters (Inc	hes)			
Tape Size	D ₀	D ₁ Minimum Note 1	E ₁	P ₀	P ₂	R Reference Note 2	S ₁ Minimum Note 3	T Maximum	T ₁ Maximum
8 mm	15.010/00	1.0 (0.039)				25.0 (0.984)			
12 mm	1.5 +0.10/-0.0 (0.059 +0.004/- 0.0)	1.5	1.75 ±0.10 (0.069 ±0.004)	4.0 ±0.10 (0.157 ±0.004)	2.0 ±0.05 (0.079 ±0.002)	30	0.600 (0.024)	0.600 (0.024)	0.100 (0.004)
16 mm	,	(0.059)				(1.181)			
	Variable Dimensions — Millimeters (Inches)								
Tape Size	Pitch	B ₁ Maximum Note 4	${\sf E_2}$ Minimum	F	P ₁	T ₂ Maximum	W Maximum	A ₀ ,B ₀	& K ₀
8 mm	Single (4 mm)	4.35 (0.171)	6.25 (0.246)	3.5 ±0.05 (0.138 ±0.002)	4.0 ±0.10 (0.157 ±0.004)	2.5 (0.098)	8.3 (0.327)		
12 mm	Single (4 mm) & Double (8 mm)	8.2 (0.323)	10.25 (0.404)	5.5 ±0.05 (0.217 ±0.002)	8.0 ±0.10 (0.315 ±0.004)	4.6 (0.181)	12.3 (0.484)	Note 5	
16 mm	Triple (12 mm)	12.1 (0.476)	14.25 (0.561)	7.5 ±0.05 (0.138 ±0.002)	12.0 ±0.10 (0.157 ±0.004)	4.6 (0.181)	16.3 (0.642)		

- 1. The embossment hole location shall be measured from the sprocket hole controlling the location of the embossment. Dimensions of embossment location and hole location shall be applied independent of each other.
- 2. The tape with or without components shall pass around R without damage (see Figure 6).
- 3. If S, < 1.0 mm, there may not be enough area for cover tape to be properly applied (see EIA Standard 481 paragraph 4.3 section b).
- 4. B, dimension is a reference dimension for tape feeder clearance only.
- 5. The cavity defined by A_{o} , B_{o} and K_{o} shall surround the component with sufficient clearance that:
 - (a) the component does not protrude above the top surface of the carrier tape.
 - (b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.
 - (c) rotation of the component is limited to 20° maximum for 8 and 12 mm tapes and 10° maximum for 16 mm tapes (see Figure 3).
 - (d) lateral movement of the component is restricted to 0.5 mm maximum for 8 and 12 mm wide tape and to 1.0 mm maximum for 16 mm tape (see Figure 4).
 - (e) for KPS Series product, A, and B, are measured on a plane 0.3 mm above the bottom of the pocket.
 - (f) see Addendum in EIA Standard 481 for standards relating to more precise taping requirements.



Figure 2 - Punched (Paper) Carrier Tape Dimensions

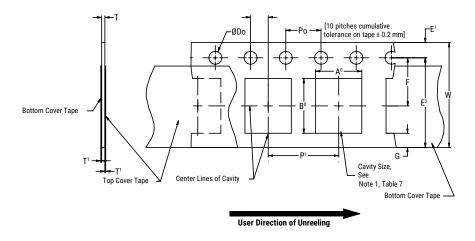


Table 7 - Punched (Paper) Carrier Tape Dimensions

Metric will govern

		Constant [Dimensions — M	illimeters (Inch	es)			
Tape Size	D ₀	E ₁	P ₀	P ₂	T ₁ Maximum	G Minimum	R Reference Note 2	
8 mm	1.5 +0.10 -0.0 (0.059 +0.004 -0.0)	1.75 ±0.10 (0.069 ±0.004)	4.0 ±0.10 (0.157 ±0.004)	2.0 ±0.05 (0.079 ±0.002)	0.10 (0.004) Maximum	0.75 (0.030)	25 (0.984)	
	Variable Dimensions — Millimeters (Inches)							
Tape Size	Pitch	E2 Minimum	F	P ₁	T Maximum	W Maximum	A_0B_0	
8 mm	Half (2 mm)	6.25	3.5 ±0.05	2.0 ±0.05 (0.079 ±0.002)	1.1	8.3 (0.327)	Note 1	
8 mm	Single (4 mm)	(0.246)	(0.138 ±0.002)	4.0 ±0.10 (0.157 ±0.004)	(0.098)	8.3 (0.327)	NOTE I	

- 1. The cavity defined by A_{n} , B_{n} and T shall surround the component with sufficient clearance that:
 - a) the component does not protrude beyond either surface of the carrier tape.
 - b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.
 - c) rotation of the component is limited to 20° maximum (see Figure 3).
 - d) lateral movement of the component is restricted to 0.5 mm maximum (see Figure 4).
 - e) see Addendum in EIA Standard 481 for standards relating to more precise taping requirements.
- 2. The tape with or without components shall pass around R without damage (see Figure 6).



Packaging Information Performance Notes

1. Cover Tape Break Force: 1.0 Kg minimum.

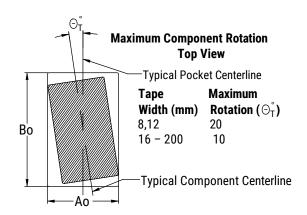
2. Cover Tape Peel Strength: The total peel strength of the cover tape from the carrier tape shall be:

Tape Width	Peel Strength
8 mm	0.1 to 1.0 Newton (10 to 100 gf)
12 and 16 mm	0.1 to 1.3 Newton (10 to 130 gf)

The direction of the pull shall be opposite the direction of the carrier tape travel. The pull angle of the carrier tape shall be 165° to 180° from the plane of the carrier tape. During peeling, the carrier and/or cover tape shall be pulled at a velocity of 300 ±10 mm/minute.

3. Labeling: Bar code labeling (standard or custom) shall be on the side of the reel opposite the sprocket holes. Refer to EIA Standards 556 and 624.

Figure 3 – Maximum Component Rotation



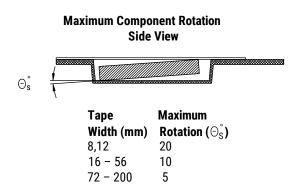


Figure 4 - Maximum Lateral Movement

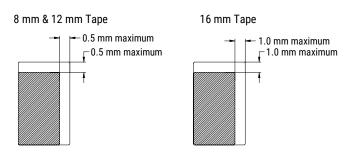


Figure 5 - Bending Radius

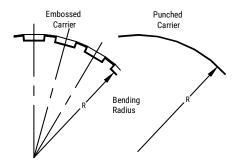
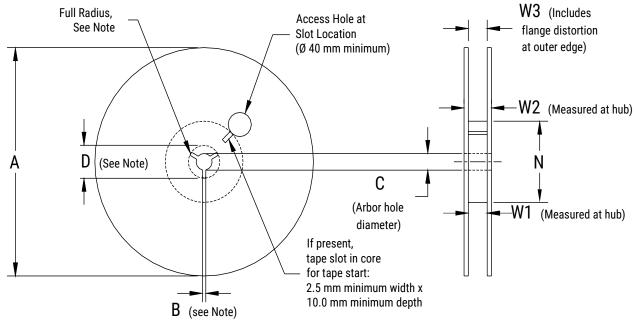




Figure 6 - Reel Dimensions



Note: Drive spokes optional; if used, dimensions B and D shall apply.

Table 8 - Reel Dimensions

Metric will govern

Constant Dimensions — Millimeters (Inches)				
Tape Size	A	B Minimum	С	D Minimum
8 mm	178 ±0.20 (7.008 ±0.008) or 330 ±0.20 (13.000 ±0.008)	1.5 (0.059)	13.0 +0.5/-0.2 (0.521 +0.02/-0.008)	20.2 (0.795)
12 mm				
16 mm				
Variable Dimensions — Millimeters (Inches)				
Tape Size	N Minimum	W ₁	W ₂ Maximum	W ₃
8 mm	50 (1.969)	8.4 +1.5/-0.0 (0.331 +0.059/-0.0)	14.4 (0.567)	Shall accommodate tape width without interference
12 mm		12.4 +2.0/-0.0 (0.488 +0.078/-0.0)	18.4 (0.724)	
16 mm		16.4 +2.0/-0.0 (0.646 +0.078/-0.0)	22.4 (0.882)	



Figure 7 - Tape Leader & Trailer Dimensions

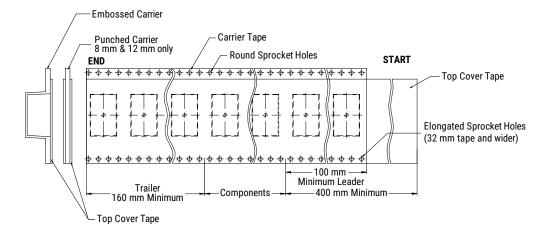
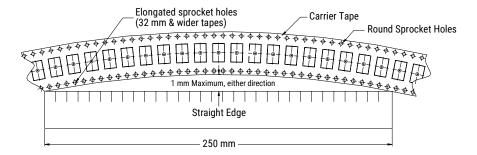


Figure 8 – Maximum Camber





KEMET Electronic Corporation Sales Offices

For a complete list of our global sales offices, please visit www.kemet.com/sales.

Disclaimer

All product specifications, statements, information and data (collectively, the "Information") in this datasheet are subject to change. The customer is responsible for checking and verifying the extent to which the Information contained in this publication is applicable to an order at the time the order is placed.

All Information given herein is believed to be accurate and reliable, but it is presented without guarantee, warranty, or responsibility of any kind, expressed or implied.

Statements of suitability for certain applications are based on KEMET Electronics Corporation's ("KEMET") knowledge of typical operating conditions for such applications, but are not intended to constitute – and KEMET specifically disclaims – any warranty concerning suitability for a specific customer application or use. The Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by KEMET with reference to the use of KEMET's products is given gratis, and KEMET assumes no obligation or liability for the advice given or results obtained.

Although KEMET designs and manufactures its products to the most stringent quality and safety standards, given the current state of the art, isolated component failures may still occur. Accordingly, customer applications which require a high degree of reliability or safety should employ suitable designs or other safeguards (such as installation of protective circuitry or redundancies) in order to ensure that the failure of an electrical component does not result in a risk of personal injury or property damage.

Although all product-related warnings, cautions and notes must be observed, the customer should not assume that all safety measures are indicted or that other measures may not be required.