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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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HAT2169H

Silicon N Channel Power MOS FET Power Switching

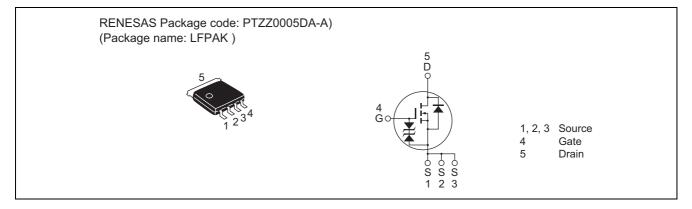
> REJ03G0119-0400 Rev.4.00 Sep 20, 2005

Features

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance

 $R_{DS(on)} = 2.8 \text{ m}\Omega \text{ typ.}$ (at $V_{GS} = 10 \text{ V}$)

Outline



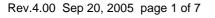
Absolute Maximum Ratings

			$(Ta = 25^{\circ}C)$
Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	40	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	ID	50	A
Drain peak current	Note1 I _{D(pulse)}	200	A
Body-drain diode reverse drain current	I _{DR}	50	A
Avalanche current	I _{AP} Note 2	30	A
Avalanche energy	E _{AR} Note 2	72	mJ
Channel dissipation	Pch Note3	30	W
Channel to Case Thermal Resistance	θch-C	4.17	°C/W
Channel temperature	Tch	150	٥C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. $PW \le 10 \ \mu s$, duty cycle $\le 1\%$

2. Value at Tch = 25° C, Rg $\geq 50 \Omega$

3. Tc = 25°C





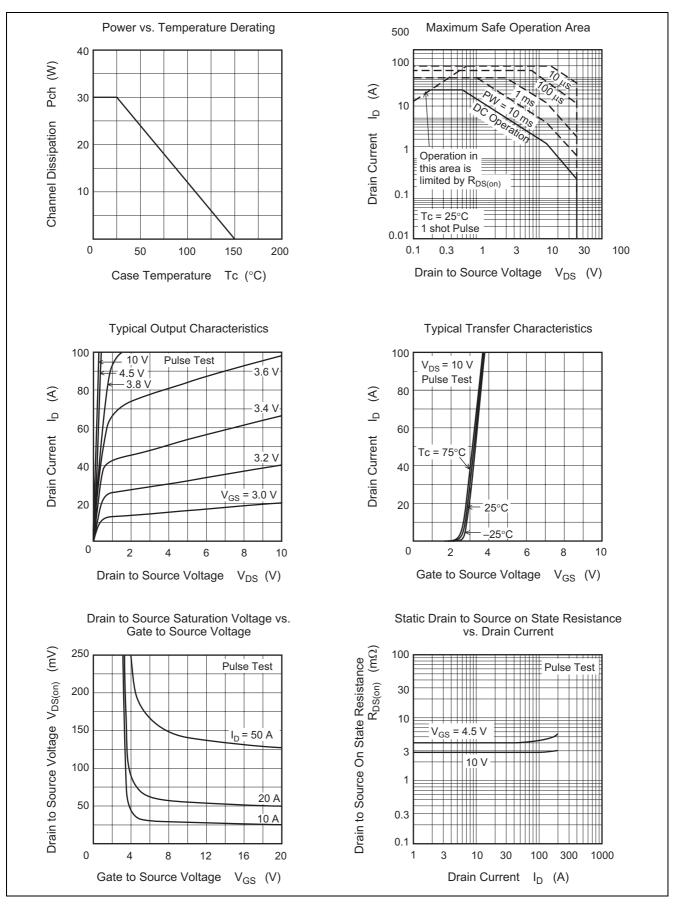
Electrical Characteristics

						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR)DSS}	40	_		V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V _{(BR)GSS}	±20	_	_	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}			±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	1	μA	$V_{DS} = 40 V, V_{GS} = 0$
Gate to source cutoff voltage	V _{GS(off)}	1.0	_	2.5	V	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$
Static drain to source on state	R _{DS(on)}	_	2.8	3.5	mΩ	$I_D = 25 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$
resistance	R _{DS(on)}	_	4.0	6.0	mΩ	$I_D = 25 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y _{fs}	39	65		S	$I_D = 25 \text{ A}, V_{DS} = 10 \text{ V}^{Note4}$
Input capacitance	Ciss	_	6650	_	pF	$V_{DS} = 10 V, V_{GS} = 0,$
Output capacitance	Coss	_	890	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss		360	_	pF	
Gate Resistance	Rg		0.5	_	Ω	
Total gate charge	Qg		45	_	nC	$V_{DD} = 10 \text{ V}, V_{GS} = 4.5 \text{ V},$ $I_{D} = 50 \text{ A}$
Gate to source charge	Qgs		21	_	nC	
Gate to drain charge	Qgd		10	_	nC	
Turn-on delay time	t _{d(on)}		15	_	ns	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 25 \text{ A},$
Rise time	tr		64	_	ns	$V_{DD} \cong 10 \text{ V}, \text{ R}_{L} = 0.4 \Omega,$ Rg = 4.7 Ω
Turn-off delay time	t _{d(off)}		55	_	ns	
Fall time	t _f	_	9.5	_	ns	
Body-drain diode forward voltage	V _{DF}	_	0.83	1.08	V	$IF = 50 A, V_{GS} = 0^{Note4}$
Body-drain diode reverse recovery	t _{rr}	_	40	_	ns	IF = 50 A, V _{GS} = 0
time						di _F / dt = 100 A/ μs

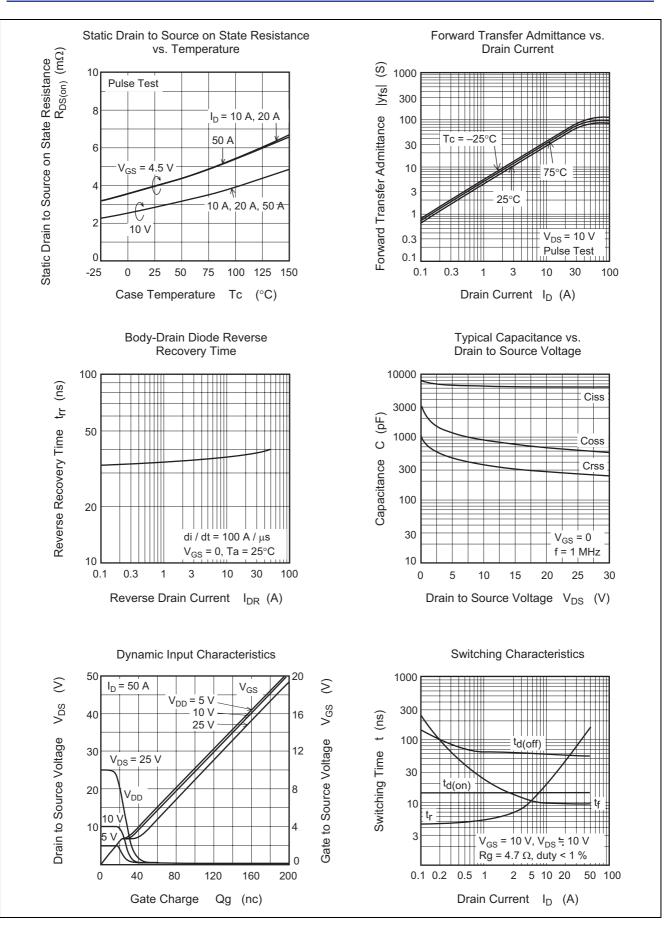
Notes: 4. Pulse test



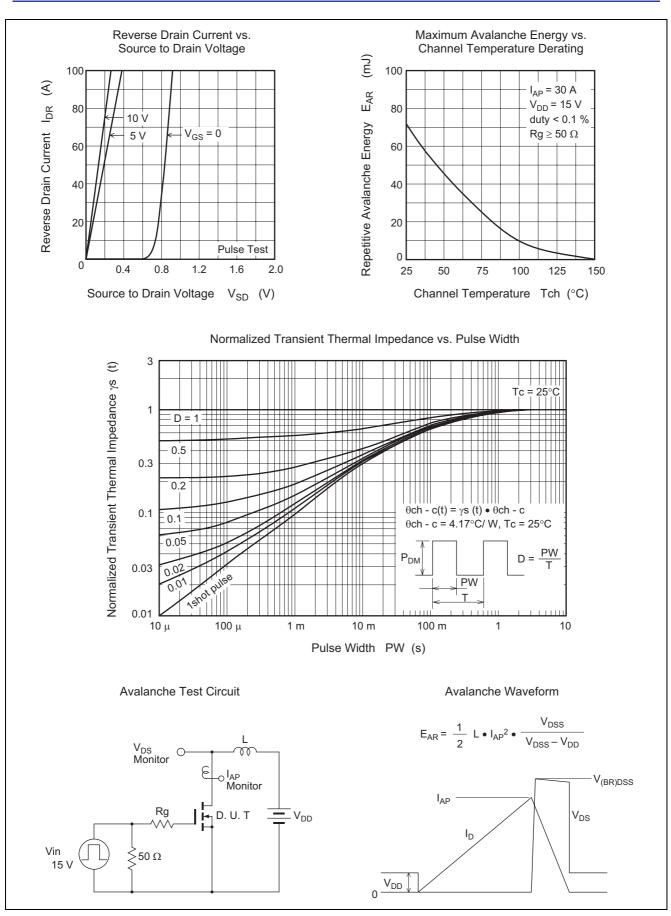
Main Characteristics



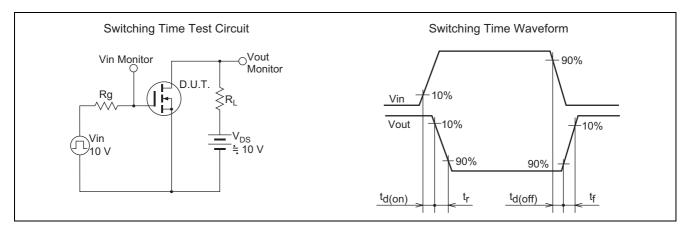






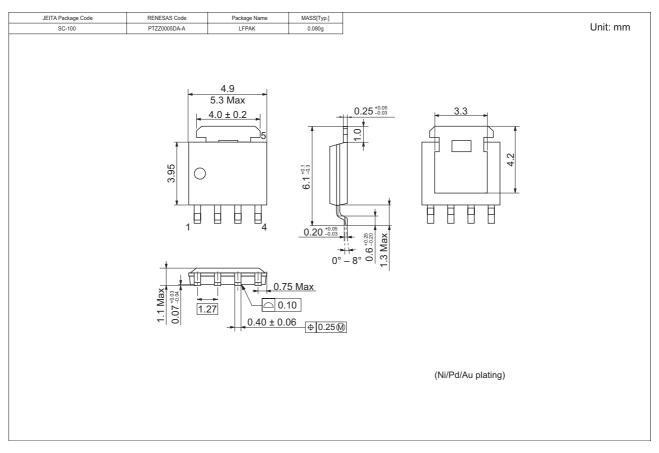








Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
HAT2169H-EL-E	2500 pcs	Taping

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